

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Applicant(s): Jagruti Viliskumar Patel

Serial No: 10/750,342

Filing Date: December 31, 2003

Attorney Docket No.: 030439

For: IDENTIFYING PROCESS AND
TEMPERATURE OF SILICON CHIPS

Examiner: Aditya S. Bhat

Art Unit: 2863

Conf. No: 9469

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

REQUEST FOR CORRECTED FILING RECEIPT

Dear Commissioner:

Upon review of the Official Filing Receipt, an error was noted in the Domestic Priority data as claimed by Applicant section. Applicants request that this error be corrected and a new Official Filing Receipt be issued.

The Domestic priority data should be replaced as follows:

This application is a Continuation-in-Part of 10/722,350 11/25/2003 which claims the benefit of 60/525,103 11/24/2003.

Applicants submit concurrently herewith an Amendment to the first paragraph of the Specification showing the continuity data as claimed by Applicant. Applicants also submit herewith a copy of the Official Filing Receipt with the requested changes noted thereon.

Applicants respectfully request that this error be corrected and a new Official Filing Receipt be issued to Applicants. Please charge any fees that may be due with this correction to Deposit Account No. 17-0026.

Dated: Nov. 17, 2008

Respectfully submitted,
By: /Jonathan T. Velasco/
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UNITED STATES PATENT AND TRADEMARK OFFICE

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/750,342	12/31/2003	2857	1202	030439	7	32	4

CONFIRMATION NO. 9469

23696
 Qualcomm Incorporated
 Patents Department
 5775 Morehouse Drive
 San Diego, CA 92121-1714

UPDATED FILING RECEIPT



OC000000013412885

Date Mailed: 08/02/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Jagrut Viliskumar Patel, San Diego, CA;
 Martin Vyungchon Choe, San Diego, CA;
 Ziad Mansour, Poway, CA;

Domestic Priority data as claimed by applicant

This appln claims benefit of 60/525,103 11/24/2003

Foreign Applications

If Required, Foreign Filing License Granted: 05/03/2004

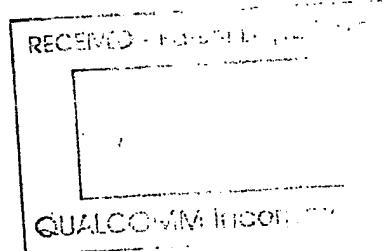
Projected Publication Date: 05/26/2005

Non-Publication Request: No

Early Publication Request: No

Title

Identifying process and temperature of silicon chips



Preliminary Class

702

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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